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Application Information

Title Line One:: VLIW Computer Processing Architecture with On-chip DRAM Usable as Physical

Title Line Three:: Memory or Cache Memory

Total Drawing Sheets::

Formal Drawings?::

No

Application Type::
Docket Number::
Secrecy Order in Patent Appl.?::

Utility

16747009910

No

Continuity Information

This application is a:: > Application One:: Filing Date:: Patent Number::

Non-Prov. of Provisional

60/187,687

March 8, 2000